CompSci 535 Course Schedule: Reading and Project Stages

1	Feb 3	Mon	Syllabus, Course Schedule, Project Overview - Intro and history
			Architecture and Instruction Sets
2	Feb 5	Wed	Appendix A
3	Feb 10	Mon	Section 1.1-1.3
			Technology
4	Feb 12	Wed	Section 1.4 - 1.7
	Feb 17	Mon	*(No class – Holiday)*
			Methodology
5	Feb 19	Wed	Section 1.8-1.13
			Memory and Cache
6	Feb 20	Thu	Section B.1 - B.3
7	Feb 24	Mon	*Draft Project Proposal due, ISA Presentations*
8	Feb 26	Wed	Section 2.1-2.3
			Pipelining
9	Mar 3	Mon	Appendix C, *Project Proposal due in final form*
			Instruction Level Parallelism and Branch Prediction
10	Mar 5	Wed	Section 3.1 - 3.5
11	Mar 10	Mon	Section 3.6-3.10
12	Mar 12	Wed	*Memory with cache and timing demo*
	Spring break		
			Multithreading and Predication
13	Mar 24	Mon	Section 3.11-3.15
			Virtualization
14	Mar 26	Wed	Section B.4-B.5
15	Mar 31	Mon	Section 2.4-2.9
16	Apr 2	Wed	*Simulation with basic instruction set, pipeline, cache, minimal UI*
			Secondary Storage
17	Apr 7	Mon	Section D.1-D.4 (online resource from book publisher)
18	Apr 9	Wed	Section 5.1-5.3
			Parallelism
19	Apr 14	Mon	Section 5.4
20	Apr 16	Wed	Section 5.5-5.11
21	Apr 18	Fri	*Full ISA demo, debug/GUI interface, assembler*
	Apr 21	Mon	*(No class – Holiday)*
			Data Parallel
22	Apr 23	Wed	Section 4.1-4.3
23	Apr 28	Mon	Section 4.4-4.10
24	Apr 30	Wed	*Benchmark demo*, *draft final report due*
			Data Centers
25	May 5	Mon	Section 6.1-6.4
26	May 7	Wed	Section 6.5-6.10
	TBD	TBD	Final Exam Period **Final Project Report Due**