535 Project ISA Design Worksheet   Name: _____

[ ] General Purpose   [ ] Special Purpose

Distinguishing features of the architecture:

[ ] Clean sheet design [ ] Variant of _____

Word size (bits): _____

Supported data types:

Operations to be supported on each type:

Registers (types and numbers):

Default number of addresses (operands) per instruction (circle):   3   2   1   0

Instruction fetch paradigm:

Memory organization (circle): Princeton   Harvard

Addressing unit (circle):   Word   Byte

Address space: 2—— bits

Addressing modes:

Load/Store instructions:

Control instructions/modes:

Condition code register (conditions):

Subroutine jump handling:

Integer ALU instructions:

Other instructions (other data types):

Instruction type field (number of bits, list of instruction types for each of the 2^n values):