

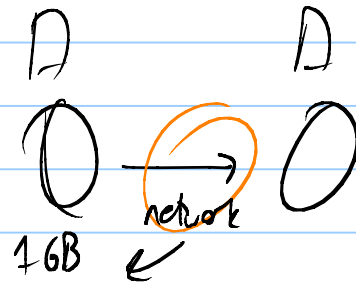
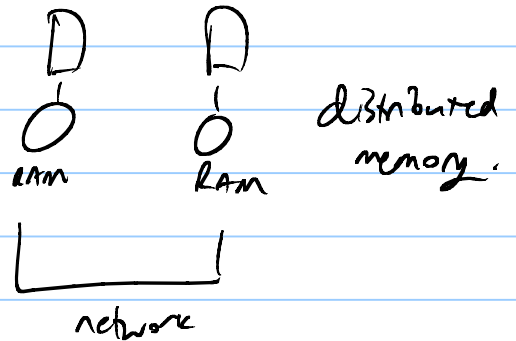
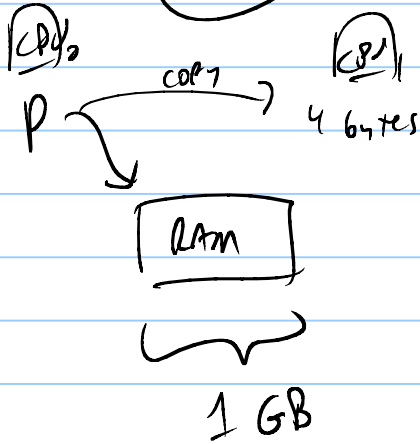
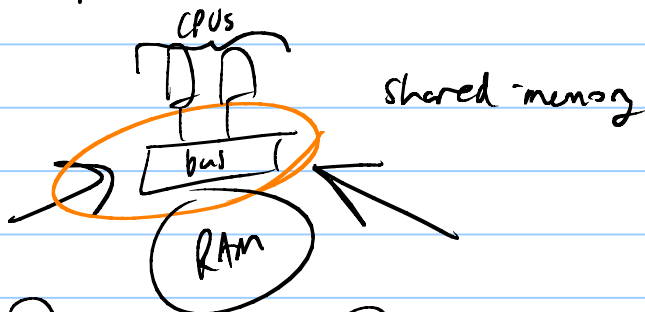
Parallel & Concurrent Prog 1

no exams

homework probs. — PLangs, libraries

one term project

parallel machines



Contention

Scalability bottleneck



Symmetric Multi Processors

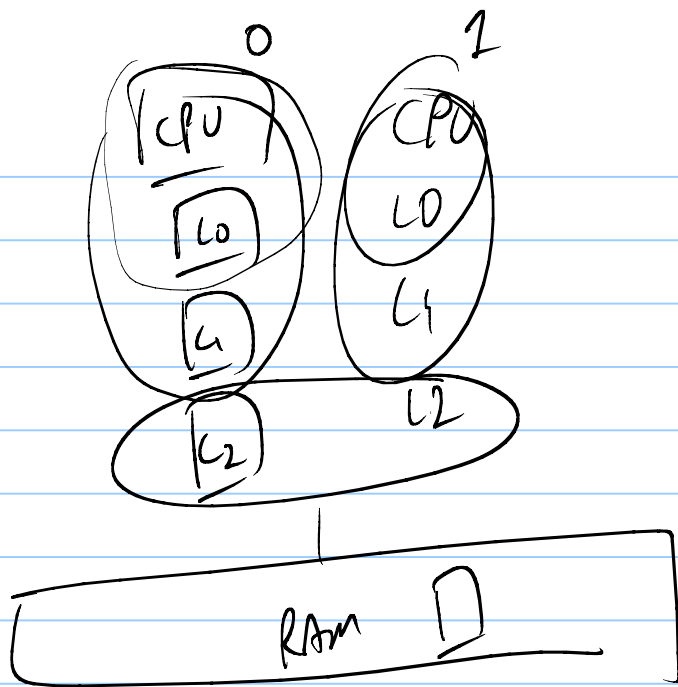
(SMP)

≤ 64 processors

NUMA

non-uniform memory access





consistent coherent

cache coherency

CC-NUMA

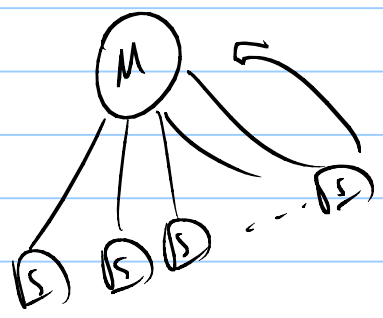
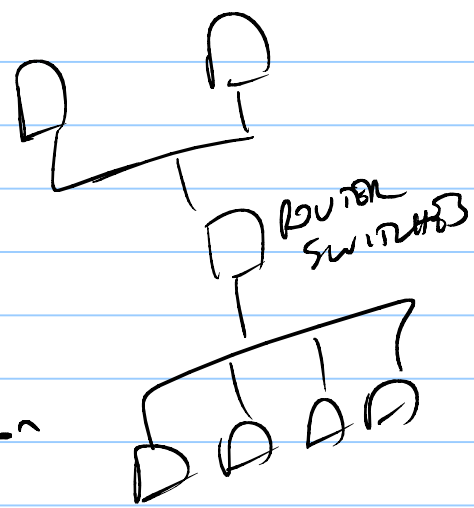
GRID

distributed memory

latency of communication is high

b/w is relatively low

master-slave model



embarrassingly parallel

- SETI@home
Folding@home

- rendering

scientific computing

divide
& conquer

- matrix mult
- FFT
- n-body problems
- SOR
- successive over-relaxation

FORTRAN

extracting ||
express || 3m

FORTRAN:

no recursion
no pointers

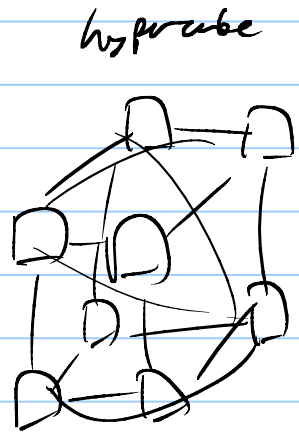
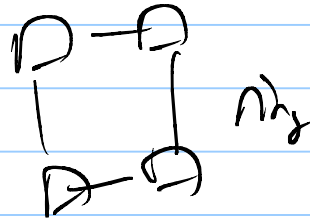
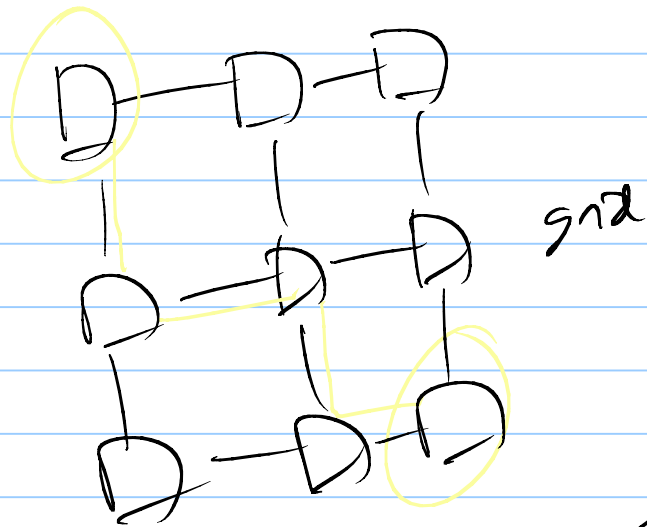
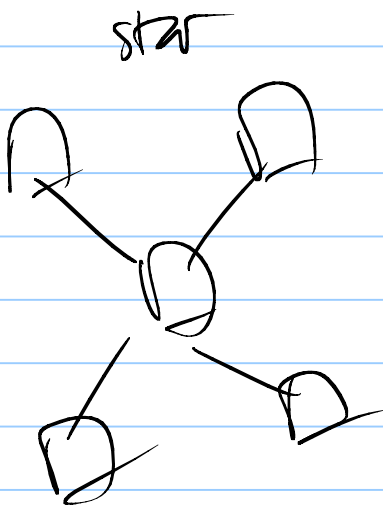
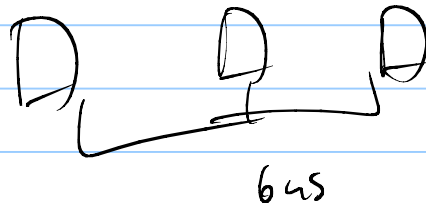
automatic ||

architectures

Procedures
common abcd...

⌘
a z
g
com z ↻

Topologies



libraries PVM, MPI

PL extensions

message-passing

MPI_Send (. . .)

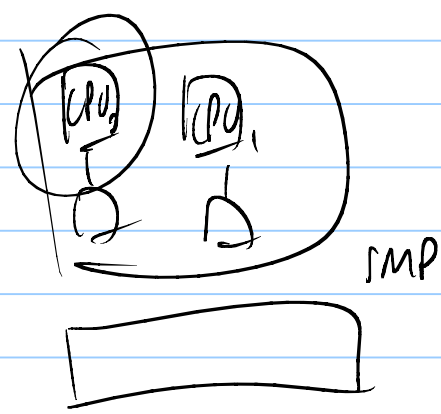
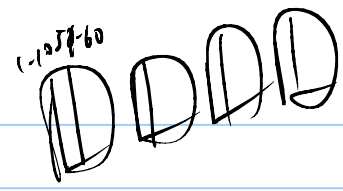
MPI_recv (. . .)

HPF
OpenMP

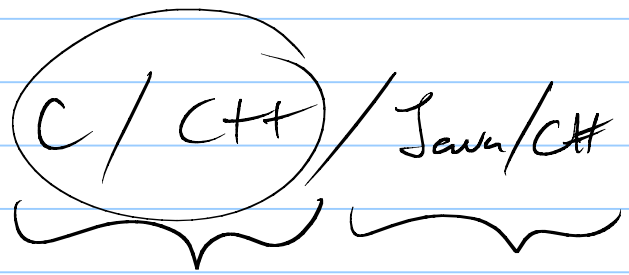
#pragma omp stripe(10)

for i = 1 to 1000

{ a[i] = a[i-1] * 2



conventional apps



threads

Everything else

Eclipse
Azureus

high-performance
low-reliability

C/C++

POSIX threads

pthread_create (&t)

pthread_join (t, result)

synchronization

Java { threads
synchronization
RL support

race condition

non-determinism

Darpa High Productivity Computing

Cray - Chapel

Sun - Fortress

IBM - X10

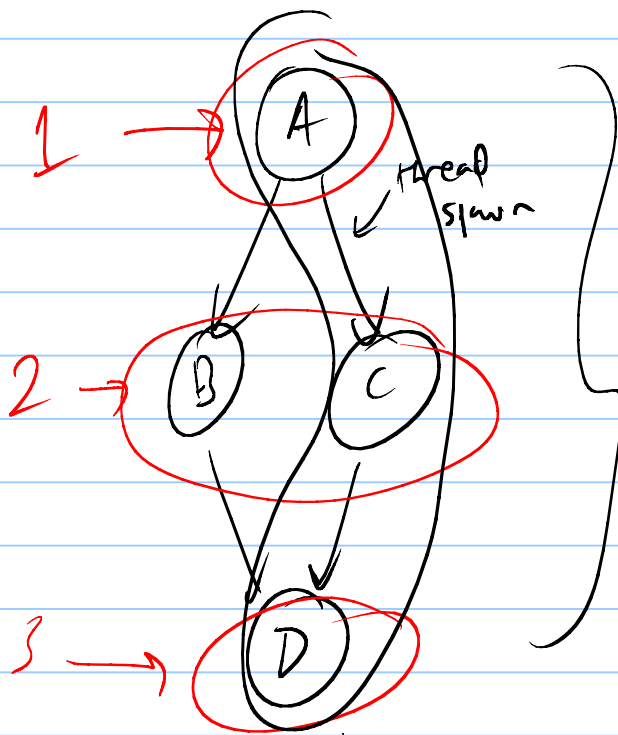
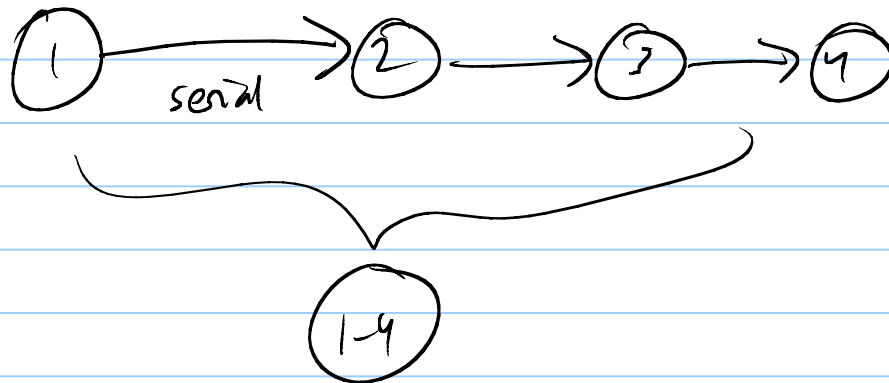
Guy Steele

Vivek Sarkar

explicit parallel langs. Java (w/threads)

implicit parallel langs.

DAGs as a computation model



program: 4 nodes

$$T_1 = \text{work} = 4$$

time
1 processor

$$\left. \frac{T_1}{P} \right\} \text{optimal parallel speedup}$$

max length
critical path

$$= T_\infty$$

$$T_P = O\left(\frac{T_1}{P}\right) + cT_\infty$$

greedy schedule / Brent schedule

"Amdahl's Law"

Cilk

```
int fib (int v) {
```

```
  if (v ≤ 1) {
```

```
    return 1;
```

```
  } else {
```

```
    int v1 = spawn fib(v-1);
```

```
    int v2 = spawn fib(v-2); sync;
```

```
    return v1 + v2;
```

```
  }
```

0 1 2 3
1, 1, 2, 3

A

D

fib(3) :

v1 = fib(2) 2

v2 = fib(1) 1

return 3.

work-stealing

