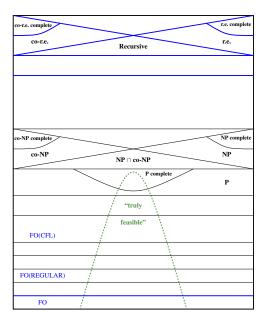
Neil Immerman

College of Computer and Information Sciences University of Massachusetts, Amherst Amherst, MA, USA

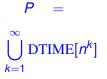
people.cs.umass.edu/~immerman

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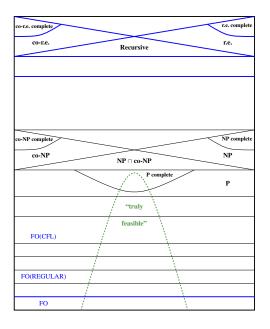


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"truly feasible" is the informal set of problems we can solve exactly on all reasonably sized instances.



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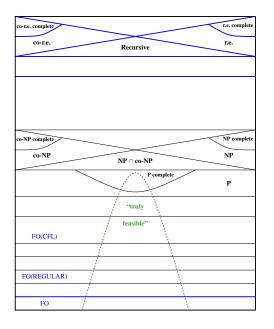


$\bigcup_{k=1}^{\infty} \text{DTIME}[n^k]$

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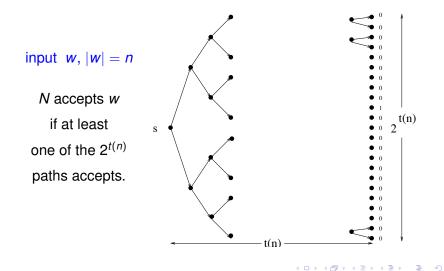
P is a good mathematical wrapper for "truly feasible".

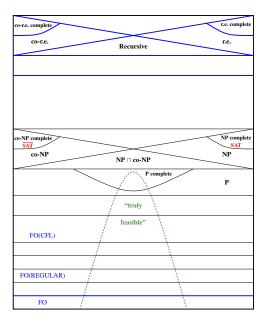
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NTIME[t(n)]: a mathematical fiction

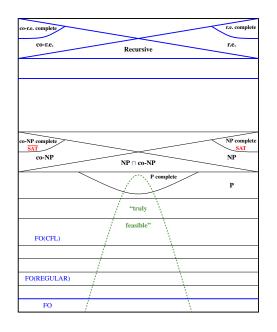




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Many optimization problems we want to solve are NP complete.

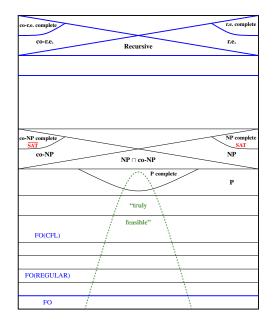
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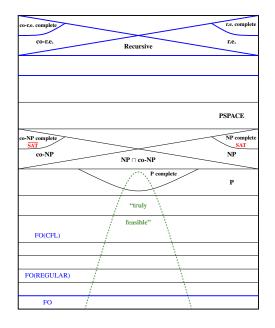
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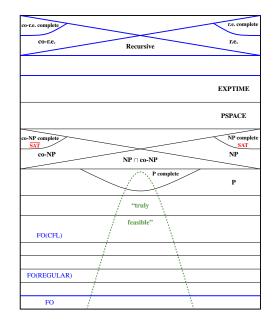
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Query
$$\rightarrow$$
Answer $q_1 \ q_2 \ \cdots \ q_n$ \mapsto $a_1 \ a_2 \ \cdots \ a_i \ \cdots \ a_m$

Neil Immerman Descriptive Complexity

Query
$$\mapsto$$
Computation \Rightarrow Answer $q_1 \ q_2 \ \cdots \ q_n$ \mapsto $a_1 \ a_2 \ \cdots \ a_i \ \cdots \ a_m$

Restrict attention to the complexity of computing individual bits of the output, i.e., **decision problems**.

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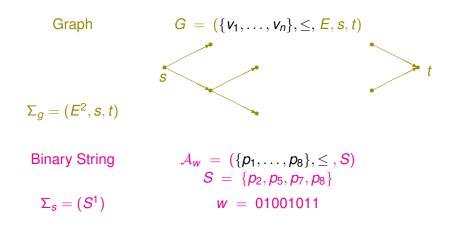
How hard is it to **check** if input has property *S*?

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There is a constructive isomorphism between these two approaches.

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Think of the Input as a Finite Logical Structure



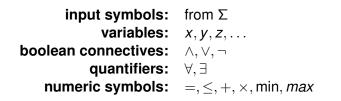
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First-Order Logic

input symbols:	from Σ
variables:	<i>X</i> , <i>Y</i> , <i>Z</i> ,
boolean connectives:	\land,\lor,\lnot
quantifiers:	\forall,\exists
numeric symbols:	$=, \leq, +, \times, \min, max$

- $\alpha \equiv \forall x \exists y (E(x, y)) \in \mathcal{L}(\Sigma_g)$
- $\beta \equiv \exists x \forall y (x \leq y \land S(x)) \in \mathcal{L}(\Sigma_s)$
- $\beta \equiv S(\min) \in \mathcal{L}(\Sigma_s)$

First-Order Logic



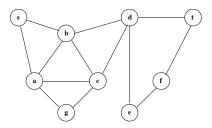
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In this setting, with the structure of interest being the **finite input**, FO is a weak, low-level complexity class.

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Second-Order Logic: FO plus Relation Variables

$\Phi_{3\text{color}} \equiv \exists \mathbf{R}^{1} G^{1} \mathbf{B}^{1} \forall x y ((\mathbf{R}(x) \lor G(x) \lor \mathbf{B}(x)) \land (\mathbf{E}(x, y) \to (\neg(\mathbf{R}(x) \land \mathbf{R}(y)) \land \neg(G(x) \land G(y)) \land \neg(\mathbf{B}(x) \land \mathbf{B}(y)))))$

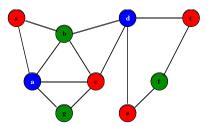


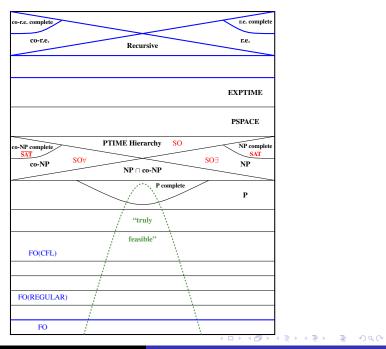
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Second-Order Logic: FO plus Relation Variables

Fagin's Theorem: $NP = SO\exists$

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 Q_+ : STRUC[Σ_{AB}] \rightarrow STRUC[Σ_s]

Neil Immerman Descriptive Complexity

 Q_+ : STRUC[Σ_{AB}] \rightarrow STRUC[Σ_s]

$$C(i) \equiv (\exists j > i) \Big(A(j) \land B(j) \land (\forall k.j > k > i) (A(k) \lor B(k)) \Big)$$

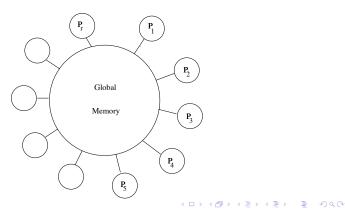
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 $Q_+(i) \equiv A(i) \oplus B(i) \oplus C(i)$

Parallel Machines:

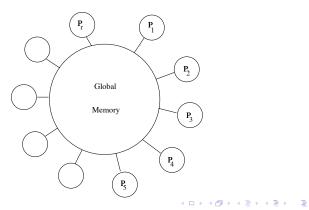
$CRAM[t(n)] = CRCW-PRAM-TIME[t(n)]-HARD[n^{O(1)}]$



Quantifiers are Parallel

$CRAM[t(n)] = CRCW-PRAM-TIME[t(n)]-HARD[n^{O(1)}]$

Assume array A[x] : x = 1, ..., r in memory.

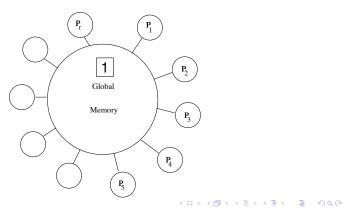


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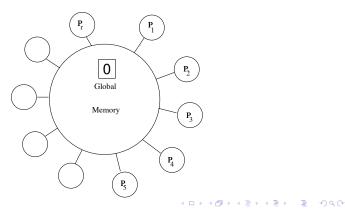
 $\forall x(A(x)) \equiv write(1);$

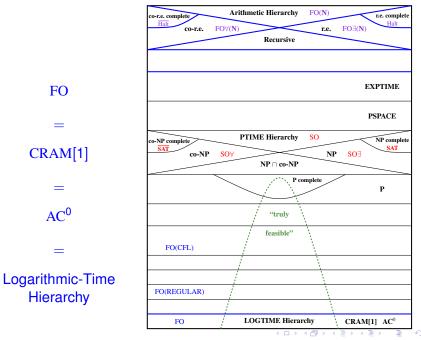


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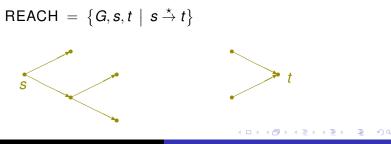
Assume array A[x] : x = 1, ..., r in memory.

 $\forall x(A(x)) \equiv \text{write}(1); \text{ proc } p_i : \text{if } (A[i] = 0) \text{ then write}(0)$

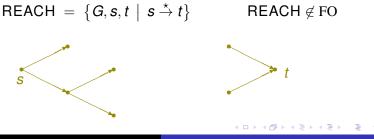




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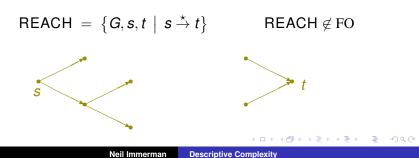


Neil Immerman Descriptive Complexity

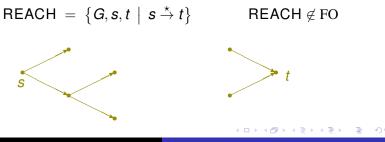


Neil Immerman Descriptive Complexity

$$E^{\star}(x,y) \stackrel{\text{def}}{=} x = y \lor E(x,y) \lor \exists z (E^{\star}(x,z) \land E^{\star}(z,y))$$



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$$\varphi_{tc}(R,x,y) \equiv x = y \lor E(x,y) \lor \exists z (R(x,z) \land R(z,y))$$

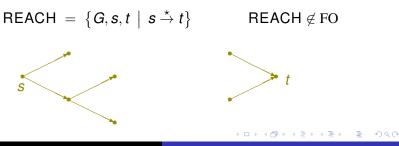


Neil Immerman Descriptive Complexity

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 φ_{tc}^{G} : binRel(G) \rightarrow binRel(G) is a monotone operator



Neil Immerman Descriptive Complexity

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$$E^{\star} = (\text{LFP}\varphi_{tc})$$
REACH = $\{G, s, t \mid s \stackrel{\star}{\to} t\}$
REACH \notin FO

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$$G \in \text{REACH} \Leftrightarrow G \models (\text{LFP}\varphi_{tc})(s,t) \qquad E^{\star} = (\text{LFP}\varphi_{tc})$$

$$\text{REACH} = \{G, s, t \mid s \stackrel{\star}{\rightarrow} t\}$$

$$\text{REACH} \notin \text{FO}$$

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Suppose $\varphi(F) = F$. By induction on *r*, for all *r*, $I^r \subseteq F$.

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inductive case: Assume $I^j \subseteq F$

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Suppose $\varphi(F) = F$. By induction on r, for all $r, I^r \subseteq F$. base case: $I^0 = \emptyset \subseteq F$.

inductive case: Assume $I^j \subseteq F$

By monotonicity, $\varphi(I^{j}) \subseteq \varphi(F)$, i.e., $I^{j+1} \subseteq F$.

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Thm. If $\varphi : \operatorname{Rel}^k(G) \to \operatorname{Rel}^k(G)$ is monotone, then $\operatorname{LFP}(\varphi)$ exists and can be computed in P.

proof: Monotone means, for all $R \subseteq S$, $\varphi(R) \subseteq \varphi(S)$.

Let
$$I^0 \stackrel{\text{def}}{=} \emptyset$$
; $I^{r+1} \stackrel{\text{def}}{=} \varphi(I^r)$ Thus, $\emptyset = I^0 \subseteq I^1 \subseteq \cdots \subseteq I^t$.

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Thus $I^t \subseteq F$ and $I^t = LFP(\varphi)$.

$$\varphi_{tc}(R, x, y) \equiv x = y \lor E(x, y) \lor \exists z (R(x, z) \land R(z, y))$$

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$$I^{1} = \varphi_{tc}^{G}(\emptyset) = \{(a, b) \in V^{G} \times V^{G} \mid \operatorname{dist}(a, b) \leq 1\}$$

$$\begin{array}{lll} \varphi_{tc}(R,x,y) &\equiv & x = y \ \lor \ E(x,y) \ \lor \ \exists z (R(x,z) \land R(z,y)) \\ I^1 = \varphi^G_{tc}(\emptyset) &= & \left\{ (a,b) \in V^G \times V^G \ \big| \ \operatorname{dist}(a,b) \leq 1 \right\} \\ I^2 = (\varphi^G_{tc})^2(\emptyset) &= & \left\{ (a,b) \in V^G \times V^G \ \big| \ \operatorname{dist}(a,b) \leq 2 \right\} \end{array}$$

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$\varphi_{tc}(R, x, y) \equiv x = y \lor E(x, y) \lor \exists z (R(x, z) \land R(z, y))$

1. Dummy universal quantification for base case:

$$\varphi_{tc}(R, x, y) \equiv (\forall z. M_1)(\exists z)(R(x, z) \land R(z, y))$$
$$M_1 \equiv \neg(x = y \lor E(x, y))$$

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2. Using \forall , replace two occurrences of *R* with one:

$$\varphi_{tc}(R, x, y) \equiv (\forall z.M_1)(\exists z)(\forall uv.M_2)R(u, v)$$
$$M_2 \equiv (u = x \land v = z) \lor (u = z \land v = y)$$

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3. Requantify x and y.

$$M_3 \equiv (x = u \land y = v)$$

 $\varphi_{tc}(R, x, y) \equiv [(\forall z.M_1)(\exists z)(\forall uv.M_2)(\exists xy.M_3)] R(x, y)$

Every FO inductive definition is equivalent to a quantifier block.

$\overline{\mathrm{QB}_{tc}} \equiv [(\forall z.M_1)(\exists z)(\forall uv.M_2)(\forall xy.M_3)]$

$\varphi_{tc}(R, x, y) \equiv [(\forall z.M_1)(\exists z)(\forall uv.M_2)(\exists xy.M_3)]R(x, y)$

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 $\varphi_{tc}(\boldsymbol{R}, \boldsymbol{x}, \boldsymbol{y}) \equiv [QB_{tc}]\boldsymbol{R}(\boldsymbol{x}, \boldsymbol{y})$

$QB_{tc} \equiv [(\forall z.M_1)(\exists z)(\forall uv.M_2)(\forall xy.M_3)]$

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Thus, for any structure $\mathcal{A} \in \text{STRUC}[\Sigma_g]$,

$$\begin{split} \mathcal{A} \in \mathsf{REACH} & \Leftrightarrow \quad \mathcal{A} \models (\mathrm{LFP}\varphi_{\mathit{tc}})(\mathit{s}, t) \\ & \Leftrightarrow \quad \mathcal{A} \models ([\mathrm{QB}_{\mathit{tc}}]^{\lceil 1 + \log \|\mathcal{A}\| \rceil} \, \mathsf{false})(\mathit{s}, t) \end{split}$$

- CRAM[t(n)] = concurrent parallel random access machine;polynomial hardware, parallel time <math>O(t(n))
 - IND[t(n)] = first-order, depth t(n) inductive definitions
 - FO[t(n)] = t(n) repetitions of a block of restricted quantifiers:

$$QB = [(Q_1 x_1.M_1) \cdots (Q_k x_k.M_k)]; M_i$$
 quantifier-free

$$\varphi_n = \underbrace{[QB][QB]\cdots[QB]}_{t(n)}M_0$$

Thm. For all constructible, polynomially bounded t(n),

$$\operatorname{CRAM}[t(n)] = \operatorname{IND}[t(n)] = \operatorname{FO}[t(n)]$$

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CRAM[t(n)] = IND[t(n)] = FO[t(n)]

proof idea: CRAM[t(n)] \supseteq FO[t(n)]: For QB with k variables, keep in memory current value of formula on all possible assignments, using n^k bits of global memory.

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 $CRAM[t(n)] \subseteq FO[t(n)]$: Inductively define new state of every bit of every register of every processor in terms of this global state at the previous time step.

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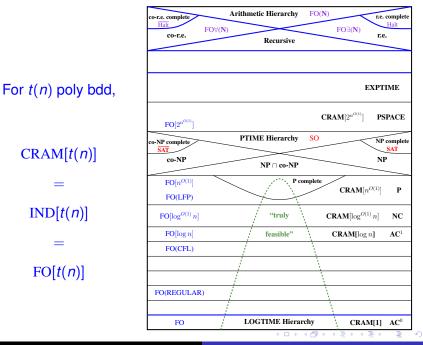
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Thm. For all t(n), even beyond polynomial,

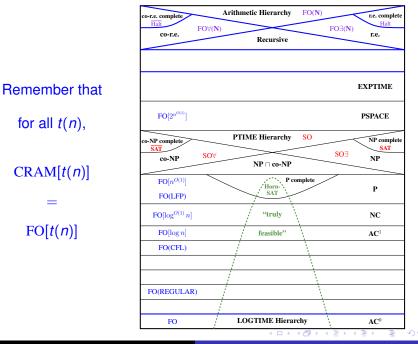
CRAM[t(n)] = FO[t(n)]

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Neil Immerman

Descriptive Complexity



Neil Immerman

Number of Variables Determines Amount of Hardware

Thm. For $k = 1, 2, ..., DSPACE[n^k] = VAR[k + 1]$

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Since variables range over a universe of size *n*, a constant number of variables can specify a polynomial number of gates.

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The proof is just a more detailed look at CRAM[t(n)] = FO[t(n)].

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A bounded number, k, of variables, is $k \log n$ bits and corresponds to n^k gates, i.e., polynomially much hardware.

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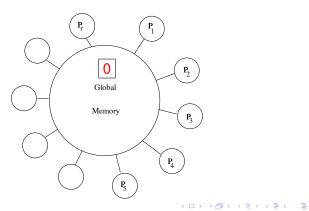
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A bounded number, k, of variables, is $k \log n$ bits and corresponds to n^k gates, i.e., polynomially much hardware.

A second-order variable of arity r is n^r bits, corresponding to 2^{n^r} gates.

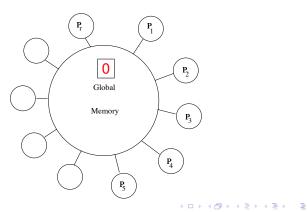
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Given φ with *n* variables and *m* clauses, is $\varphi \in 3$ -SAT?

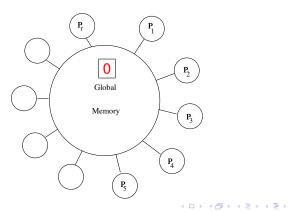


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With $r = m2^n$ processors, recognize 3-SAT in constant time!



Given φ with *n* variables and *m* clauses, is $\varphi \in 3$ -SAT? With $r = m2^n$ processors, recognize 3-SAT in constant time! Let *S* be the first *n* bits of our processor number.



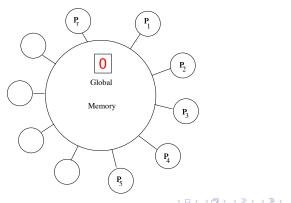
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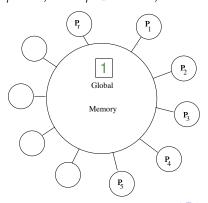
If processors $S1, \ldots Sm$ notice that truth assignment S makes all m clauses of φ true, then $\varphi \in 3$ -SAT,



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Thm. SO[t(n)] = CRAM[t(n)]-HARD[$2^{n^{O(1)}}$].

Neil Immerman Descriptive Complexity

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Thm. SO[t(n)] = CRAM[t(n)]-HARD[$2^{n^{O(1)}}$].

proof: SO[t(n)] is like FO[t(n)] but using a quantifier block containing both first-order and second-order quantifiers. The proof is similar to FO[t(n)] = CRAM[t(n)].

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Cor.

SO = PTIME Hierarchy =
$$CRAM[1]$$
-HARD $[2^{n^{O(1)}}]$

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SO = PTIME Hierarchy = CRAM[1]-HARD[$2^{n^{O(1)}}$] SO[$n^{O(1)}$] = PSPACE = CRAM[$n^{O(1)}$]-HARD[$2^{n^{O(1)}}$]

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SO	=	PTIME Hierarchy	=	CRAM[1]-HARD[2 ^{n^{O(1)}]}
SO[<i>n</i> ^{O(1)}]	=	PSPACE	=	$CRAM[n^{O(1)}]-HARD[2^{n^{O(1)}}]$
no[0 ^{<i>n</i>^{O(1)}]}				$CD \wedge M(O^{O(1)})$ I I A D $D(O^{O(1)})$

 $SO[2^{n^{O(1)}}] = EXPTIME = CRAM[2^{n^{O(1)}}]-HARD[2^{n^{O(1)}}]$

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Parallel Time versus Amount of Hardware

 $PSPACE = FO[2^{n^{O(1)}}] = CRAM[2^{n^{O(1)}}] - HARD[n^{O(1)}]$ $= SO[n^{O(1)}] = CRAM[n^{O(1)}] - HARD[2^{n^{O(1)}}]$

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$$PSPACE = FO[2^{n^{O(1)}}] = CRAM[2^{n^{O(1)}}] - HARD[n^{O(1)}]$$
$$= SO[n^{O(1)}] = CRAM[n^{O(1)}] - HARD[2^{n^{O(1)}}]$$

We would love to understand this tradeoff.

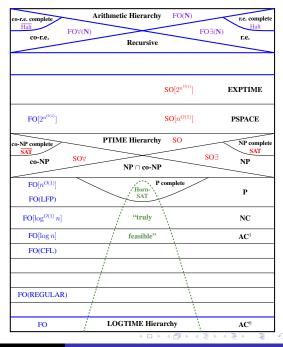
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- ► Is there such a thing as an inherently sequential problem?, i.e., is NC ≠ P?

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- We would love to understand this tradeoff.
- ► Is there such a thing as an inherently sequential problem?, i.e., is NC ≠ P?
- Same tradeoff as number of variables vs. number of iterations of a quantifier block.



 $SO[t(n)] = CRAM[t(n)]-HARD-[2^{n^{O(1)}}]$

Recent Breakthroughs in Descriptive Complexity

Theorem [Ben Rossman] Any first-order formula with any numeric relations $(\leq, +, \times, ...)$ that means "I have a clique of size *k*" must have at least k/4 variables.

Creative new proof idea using Håstad's Switching Lemma gives the essentially optimal bound.

This lower bound is for a fixed formula, if it were for a sequence of polynomially-sized formulas, i.e., a fixed-point formula, it would follow that CLIQUE \notin P and thus P \neq NP.

Best previous bounds:

- k variables necessary and sufficient without ordering or other numeric relations [I 1980].
- Nothing was known with ordering except for the trivial fact that 2 variables are not enough.

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Recent Breakthroughs in Descriptive Complexity

Theorem [Martin Grohe] Fixed-Point Logic with Counting captures Polynomial Time on all classes of graphs with excluded minors.

Grohe proves that for every class of graphs with excluded minors, there is a constant k such that two graphs of the class are isomorphic iff they agree on all k-variable formulas in fixed-point logic with counting.

Using Ehrenfeucht-Fraïssé games, this can be checked in polynomial time, $(O(n^k(\log n)))$. In the same time we can give a canonical description of the isomorphism type of any graph in the class. Thus every class of graphs with excluded minors admits the same general polynomial time canonization algorithm: we're isomorphic iff we agree on all formulas in C_k and in particular, you are isomorphic to me iff your C_k canonical description is equal to mine.

Thm. REACH is complete for $NL = NSPACE[\log n]$.

Proof: Let $A \in NL$, $A = \mathcal{L}(N)$, uses $c \log n$ bits of worktape. Input w, n = |w|

$$w \mapsto \text{CompGraph}(N, w) = (V, E, s, t)$$

$$V = \{ \mathrm{ID} = \langle q, h, p \rangle \mid q \in \mathrm{States}(N), h \leq n, |p| \leq c \lceil \log n \rceil \}$$

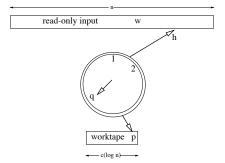
$$E = \{(\mathrm{ID}_1, \mathrm{ID}_2) \mid \mathrm{ID}_1(w) \xrightarrow{N} \mathrm{ID}_2(w)\}$$

s = initial ID

t = accepting ID

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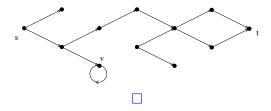
NSPACE[log n] Turing Machine



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Claim. $w \in \mathcal{L}(N) \Leftrightarrow \text{CompGraph}(N, w) \in \text{REACH}$



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$Cor: NL \subseteq P$

Proof: $\mathsf{REACH} \in \mathsf{P}$

P is closed under (logspace) reductions.

i.e.,
$$(B \in P \land A \leq B) \Rightarrow A \in P$$

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Prop. NSPACE[s(n)] \subseteq NTIME[$2^{O(s(n))}$] \subseteq DSPACE[$2^{O(s(n))}$]

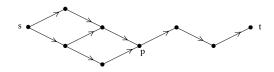
We can do much better!

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Savitch's Theorem

$\mathsf{REACH} \in \mathsf{DSPACE}(\log n)^2$



proof:

$$\begin{array}{rcl} G \in \mathsf{REACH} & \Leftrightarrow & G \models \mathsf{PATH}_n(s,t) \\ \mathsf{PATH}_1(x,y) & \equiv & x = y \ \lor \ E(x,y) \\ \mathsf{PATH}_{2d}(x,y) & \equiv & \exists z \, (\mathsf{PATH}_d(x,z) \ \land \ \mathsf{PATH}_d(z,y)) \end{array}$$

 $S_n(d)$ = space to check paths of dist. *d* in *n*-nodegraphs

$$S_n(n) = \log n + S_n(n/2)$$

= $O((\log n)^2)$

DSPACE[s(n)] \subseteq NSPACE[n] \subseteq DSPACE[(s(n))²]

proof: Let $A \in \text{NSPACE}[s(n)]; A = \mathcal{L}(N)$

$$w \in A$$
 \Leftrightarrow CompGraph $(N, w) \in \mathsf{REACH}$

$$|w| = n;$$
 $|CompGraph(N, w)| = 2^{O(s(n))}$

Testing if $CompGraph(N, w) \in REACH$ takes space,

$$(\log(|\text{CompGraph}(N, w)|))^2 = (\log(2^{O(s(n))})^2)$$

= $O((s(n))^2)$

From w build CompGraph(N, w) in DSPACE[s(n)].

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$\overline{\mathsf{REACH}} \in \mathrm{NL}$

proof: Fix G, let $N_d = |\{v \mid \text{distance}(s, v) \le d\}|$

Claim: The following problems are in NL:

1. dist(x, d): distance(s, x) $\leq d$

2. NDIST(x, d; m): if $m = N_d$ then $\neg dist(x, d)$

proof:

- 1. Guess the path of length $\leq d$ from *s* to *x*.
- 2. Guess *m* vertices, $v \neq x$, with dist(v, d).

```
c := 0;
for v := 1 to n do { // nondeterministically

(dist(v, d) && v \neq x; c + +) ||

(no-op)

}

if (c == m) then ACCEPT
```

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Claim. We can compute N_d in NL.

proof: By induction on *d*.

Base case: $N_0 = 1$

Inductive step: Suppose we have N_d .

1.
$$c := 0$$
;
2. for $v := 1$ to n do { // nondeterministically
3. (dist($v, d + 1$); $c + +$) ||
4. ($\forall z$ (NDIST($z, d; N_d$) \lor ($z \neq v \land \neg E(z, v)$)))
5. }
6. $N_{d+1} := c$

$$G \in \overline{\mathsf{REACH}} \iff \mathsf{NDIST}(t, n; N_n)$$

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Thm. NSPACE[s(n)] = co-NSPACE[s(n)]. proof: Let $A \in NSPACE[s(n)]$; $A = \mathcal{L}(N)$

$$w \in A \quad \Leftrightarrow \quad \text{CompGraph}(N, w) \in \mathsf{REACH}$$

$$|w| = n;$$
 $|CompGraph(N, w)| = 2^{O(s(n))}$

Testing if CompGraph(N, w) $\in \overline{\mathsf{REACH}}$ takes space,

$$log(|CompGraph(N, w)|) = log(2^{O(s(n))})$$
$$= O(s(n))$$

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Diagonalization: more of the same resource gives us more:

```
DTIME[n] \stackrel{\subseteq}{\neq} DTIME[n<sup>2</sup>],
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same for DSPACE, NTIME, NSPACE, ...
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 Natural Complexity Classes have Natural Complete Problems

SAT for NP, CVAL for P, QSAT for PSPACE, ...

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 Natural Complexity Classes have Natural Complete Problems

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 Major Missing Idea: concept of work or conservation of energy in computation, i.e,

in order to solve SAT or other hard problem we must do a certain amount of computational work.

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Strong Lower Bounds on FO[t(n)] for small t(n)

► [Sipser]: strict first-order alternation hierarchy: FO.

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- ► [Sipser]: strict first-order alternation hierarchy: FO.
- [Beame-Håstad]: hierarchy remains strict up to FO[log n/ log log n].

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- [Beame-Håstad]: hierarchy remains strict up to FO[log n/ log log n].
- $NC^1 \subseteq FO[\log n / \log \log n]$ and this is tight.
- ► Does REACH require FO[log n]? This would imply NC¹ ≠ NL.

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Much is known about approximation, e.g., some NP complete problems, e.g., Knapsack, Euclidean TSP, can be approximated as closely as we want, others, e.g., Clique, can't be.

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- Basic trade-offs are not understood, e.g., trade-off between time and number of processors. Are any problems inherently sequential? How can we best use mulitcores?
- SAT solvers are impressive new general purpose problem solvers, e.g., used in model checking, AI planning, code synthesis. How good are current SAT solvers? How much can they be improved?

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Descriptive Complexity

Fact: For constructible t(n), FO[t(n)] = CRAM[t(n)]

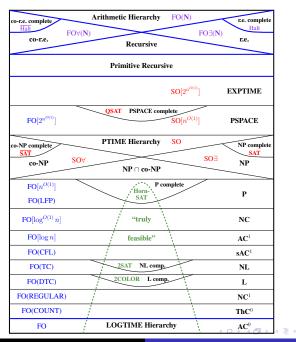
Fact: For $k = 1, 2, ..., VAR[k + 1] = DSPACE[n^k]$

The complexity of computing a query is closely tied to the complexity of describing the query.

$$P = NP \Leftrightarrow FO(LFP) = SO$$

 $ThC^0 = NP \Leftrightarrow FO(MAJ) = SO$
 $P = PSPACE \Leftrightarrow FO(LFP) = SO(TC)$

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