

# Efficiently Reasoning about Programs

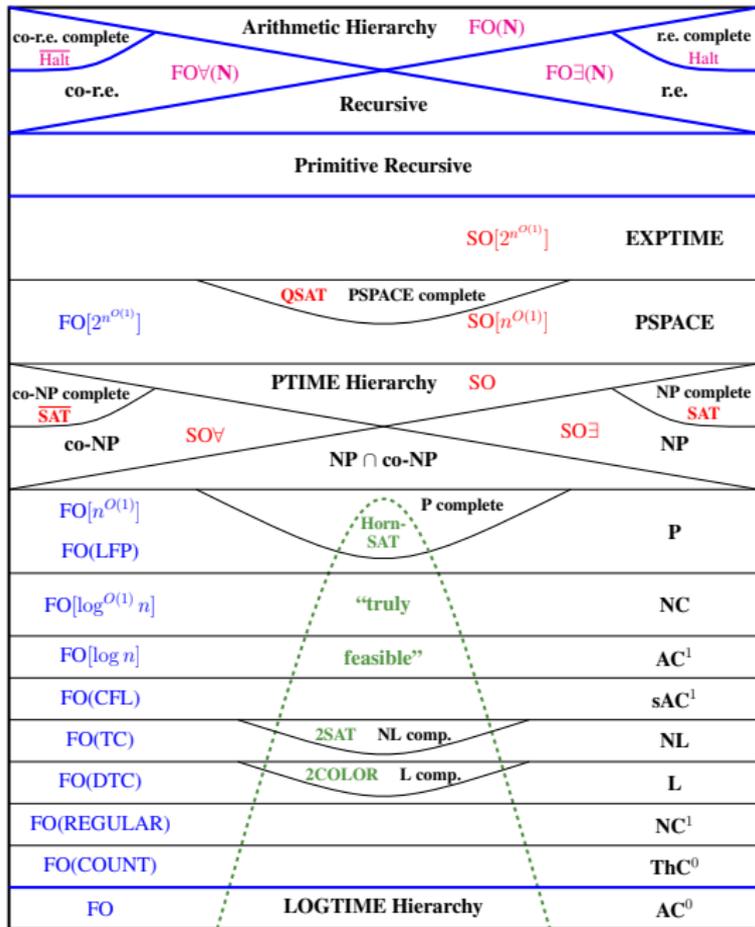
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Amherst, MA, USA

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co-r.e. complete $\overline{\text{Halt}}$	Arithmetic Hierarchy		FO(N)	r.e. complete $\text{Halt}$
co-r.e.	$\text{FO}\forall(N)$	Recursive	$\text{FO}\exists(N)$	r.e.
Primitive Recursive				
			$\text{SO}[2^{n^{O(1)}}]$	EXPTIME
$\text{FO}[2^{n^{O(1)}}]$	QSAT	PSPACE complete	$\text{SO}[n^{O(1)}]$	PSPACE
co-NP complete $\overline{\text{SAT}}$	PTIME Hierarchy		SO	NP complete $\text{SAT}$
co-NP	$\text{SO}\forall$	$\text{NP} \cap \text{co-NP}$	$\text{SO}\exists$	NP
$\text{FO}[n^{O(1)}]$	P complete			P
$\text{FO}(\text{LFP})$	Horn-SAT			
$\text{FO}[\log^{O(1)} n]$	"truly feasible"			NC
$\text{FO}[\log n]$				$\text{AC}^1$
$\text{FO}(\text{CFL})$				$\text{sAC}^1$
$\text{FO}(\text{TC})$	2SAT	NL comp.		NL
$\text{FO}(\text{DTC})$	2COLOR	L comp.		L
$\text{FO}(\text{REGULAR})$				$\text{NC}^1$
$\text{FO}(\text{COUNT})$				$\text{ThC}^0$
FO	LOGTIME Hierarchy			$\text{AC}^0$

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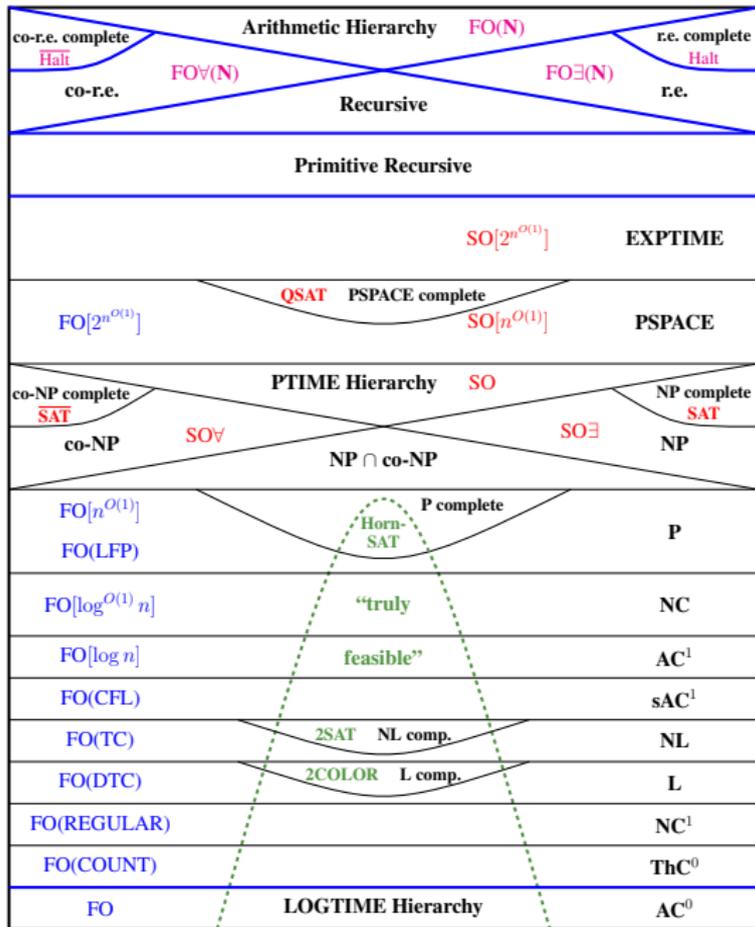
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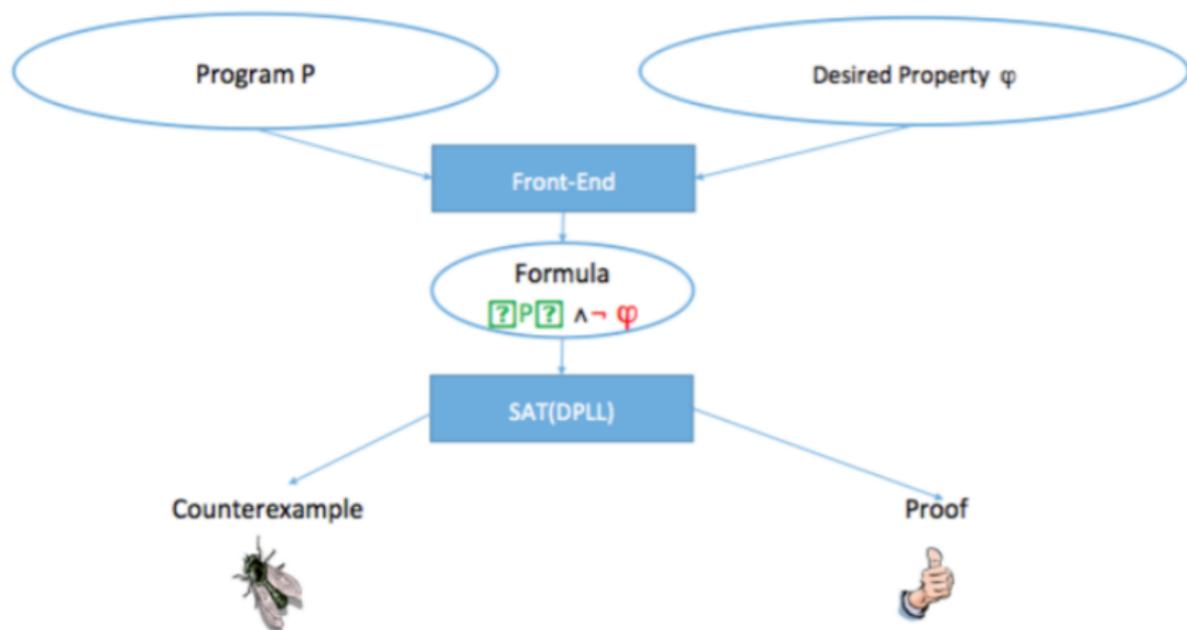
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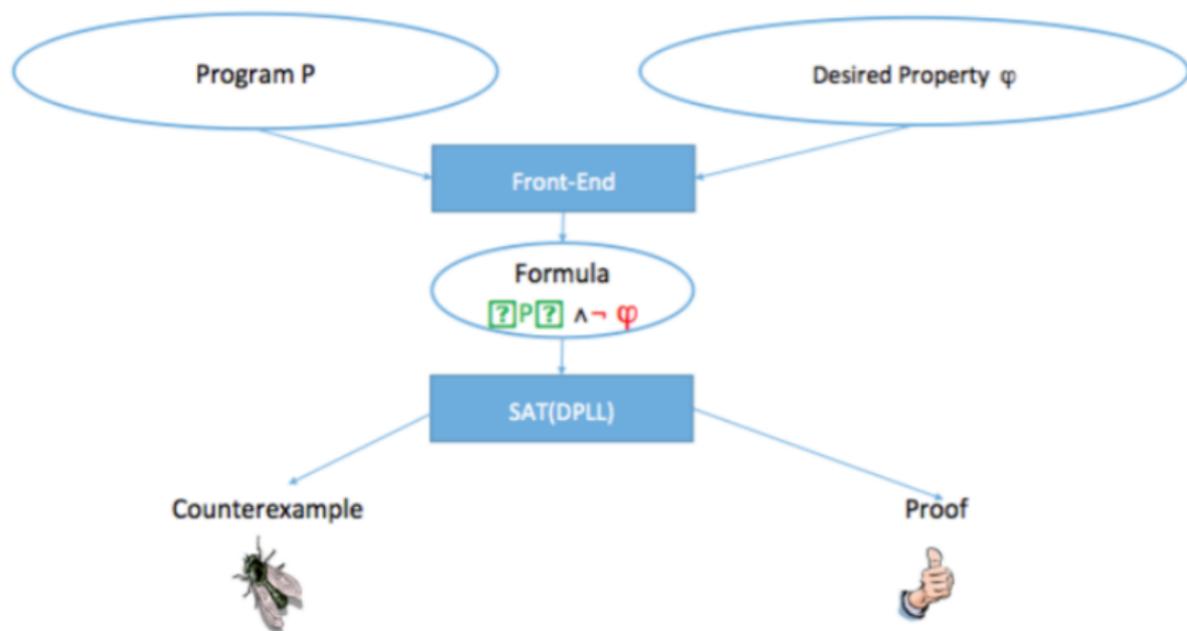
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- ▶ Great progress in design of **SAT Solvers**.
  - ▶ **Fast, general-purpose problem solvers**.

# Verification by Reduction to SAT

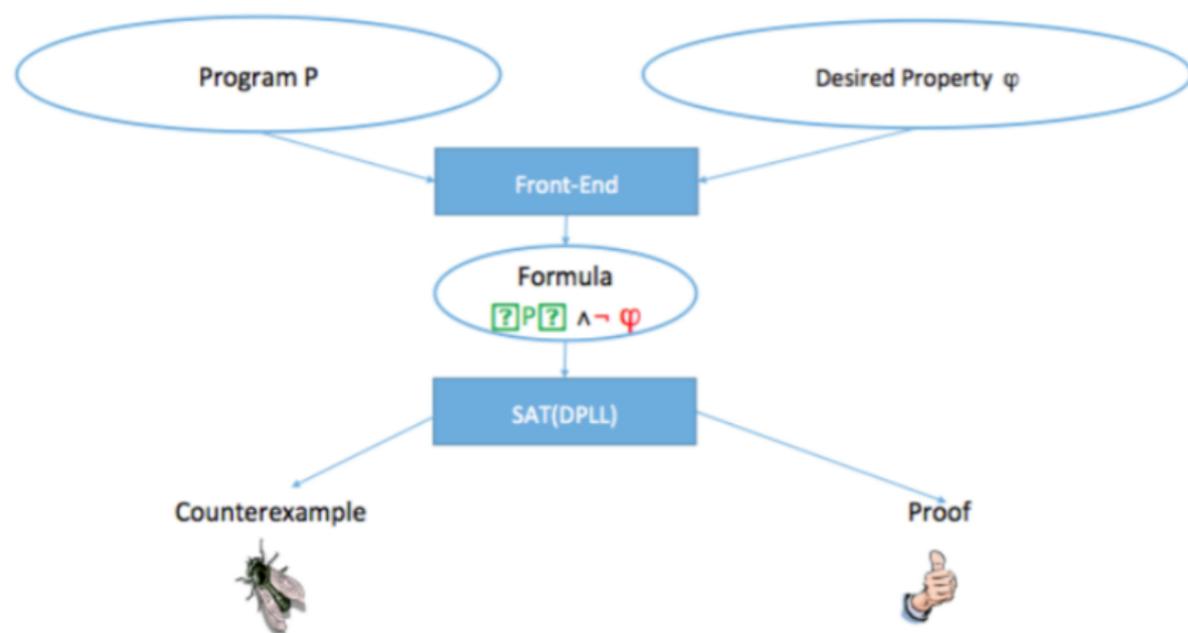


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4. What **additional information** should we maintain? — **auxiliary data structure**

# Dynamic (Incremental) Applications

- ▶ Databases
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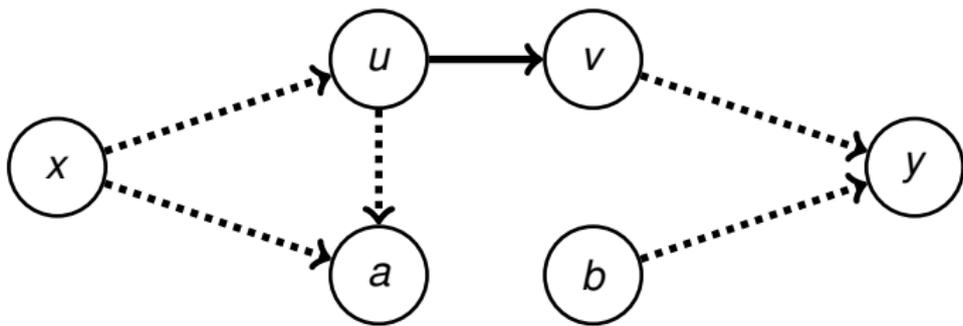
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**connectivity,**  
**minimum spanning trees,**  
 **$k$ -edge connectivity, ...**

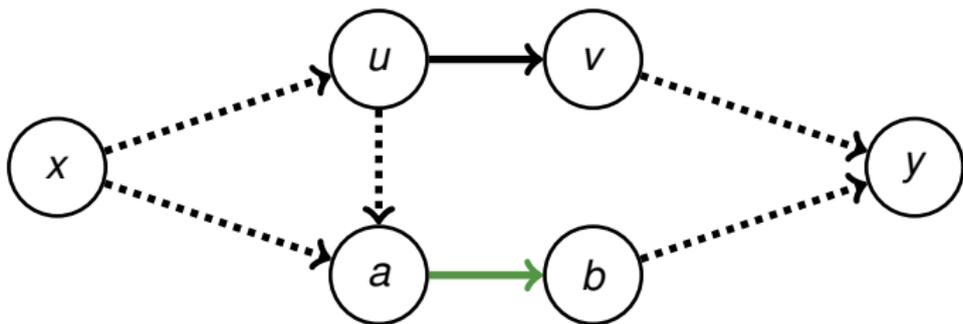
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**Fact:** [Dong & Su]  $\text{REACH}(\text{acyclic}) \in \text{DynFO}$



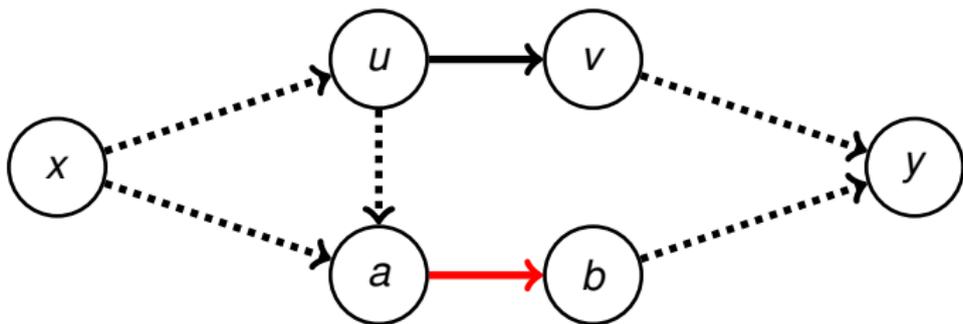
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$$P'(x, y) \equiv P(x, y) \wedge \left[ \neg(P(x, a) \wedge P(b, y)) \right. \\ \left. \vee (\exists uv)(P(x, u) \wedge E(u, v) \wedge P(v, y) \right. \\ \left. \wedge P(u, a) \wedge \neg P(v, a) \wedge (a \neq u \vee b \neq v)) \right]$$

# Reachability Problems

REACH =  $\{G \mid G \text{ directed, } s \xrightarrow{*}_G t\}$  NL

REACH<sub>d</sub> =  $\{G \mid G \text{ directed, outdegree} \leq 1, s \xrightarrow{*}_G t\}$  L

REACH<sub>u</sub> =  $\{G \mid G \text{ undirected, } s \xrightarrow{*}_G t\}$  L

REACH<sub>a</sub> =  $\{G \mid G \text{ alternating, } s \xrightarrow{*}_G t\}$  P

# Facts about dynamic REACHABILITY Problems:

$\text{REACH}(\text{acyclic}) \in \text{Dyn-FO}$  [DS]

$\text{REACH}_d \in \text{Dyn-QF}$  [H]

$\text{REACH}_u \in \text{Dyn-FO}$  [PI]

$\text{REACH} \in \text{Dyn-FO}(\text{COUNT})$  [H]

$\text{PAD}(\text{REACH}_a) \in \text{Dyn-FO}$  [PI]

# Exciting New Result

**Thm.** REACH  $\in$  Dyn-FO

[Samir Datta, Raghav Kulkarni, Anish Mukherjee, Thomas Schwentick, Thomas Zeume]

<http://arxiv.org/abs/1502.07467>

REACH  $\leq$  **Matrix Rank**  $\in$  Dyn-FO

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**proof:** Maintain  $E, E^*, D$  (outdegree = 1).

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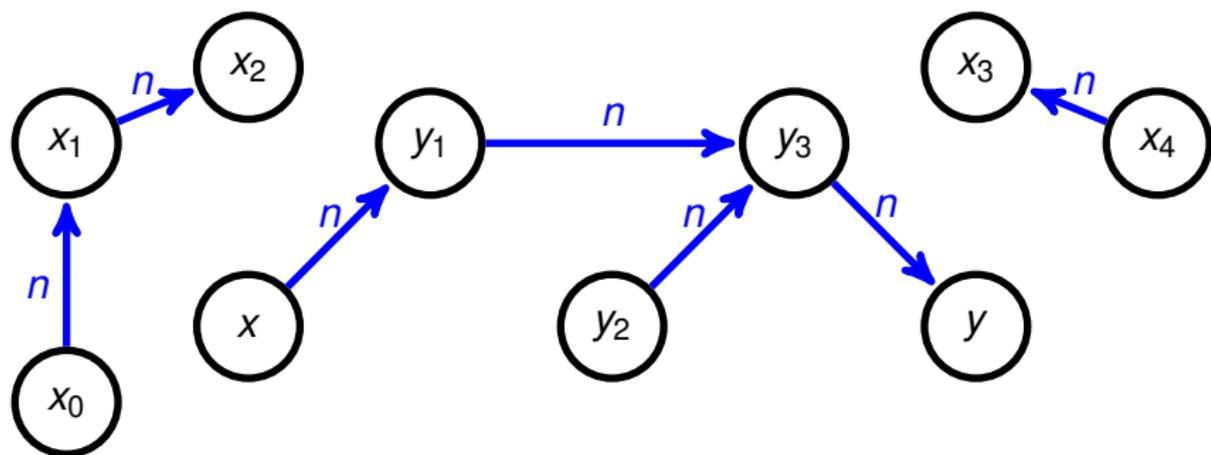
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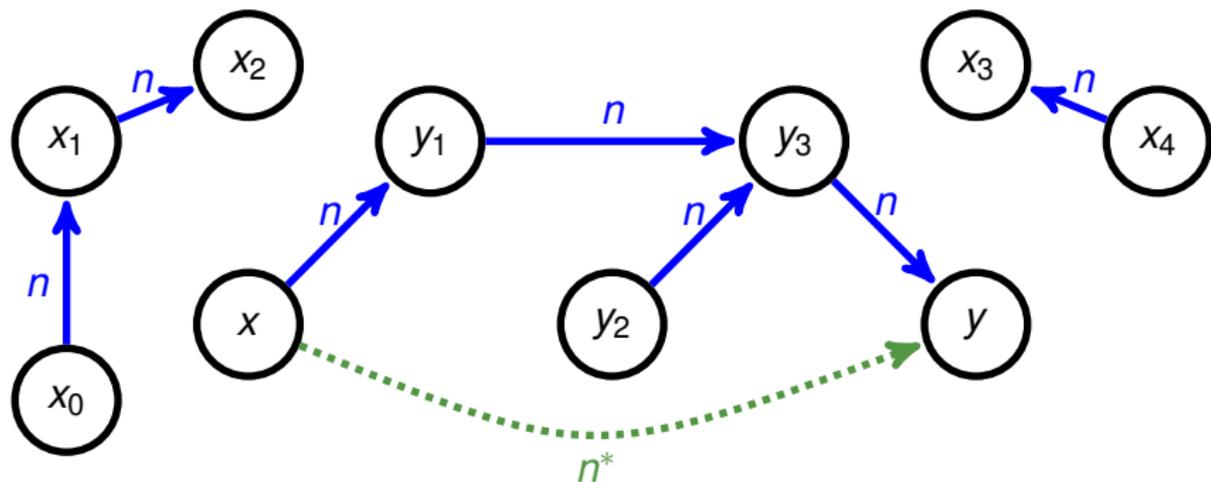
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**Reasoning About reachability** – can we get to  $y$  from  $x$  by following a sequence of pointers – is **crucial** for **understanding** programs and **proving** that they meet their specifications.



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- ▶ Can express tilings and thus runs of Turing Machines.
- ▶ Even worse, can express **finite path** and thus **finite** and thus **standard natural numbers**. Thus satisfiability of FO(TC) is as hard as the Arithmetic Hierarchy [Avron].

Itzhaky, Banerjee, Immerman, Aleks Nanevski, Sagiv,  
“Effectively-Propositional Reasoning About Reachability in  
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$$\mathbf{linear} \equiv \forall xyz (n^*(x, y) \wedge n^*(x, z) \rightarrow n^*(y, z) \vee n^*(z, y))$$

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- ▶ The negation of the correctness condition is  $\exists\forall$ , thus equi-satisfiable with a propositional formula (**EPR**).
- ▶ Use a SAT solver to automatically prove correctness or find counter-example runs, typically in **only a few seconds**.

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- ▶ Z3 seems to do very well for us on **EPR-SAT**.

Benchmark	Formula Size						Solving time (Z3)
	P,Q		Inv		VC		
	#	∇	#	∇	#	∇	
SLL: reverse	2	2	11	2	133	3	57ms
SLL: filter	5	1	14	1	280	4	39ms
SLL: create	1	0	1	0	36	3	13ms
SLL: delete	5	0	12	1	152	3	23ms
SLL: deleteAll	3	2	7	2	106	3	32ms
SLL: insert	8	1	6	1	178	3	17ms
SLL: find	7	1	7	1	64	3	15ms
SLL: last	3	0	5	0	74	3	15ms
SLL: merge	14	2	31	2	2255	3	226ms
SLL: rotate	6	1	-	-	73	3	22ms
SLL: swap	14	2	-	-	965	5	26ms
DLL: fix	5	2	11	2	121	3	32ms
DLL: splice	10	2	-	-	167	4	27ms

**Thm. 2** [Hesse] Reachability of functional graphs is in DynQF.

**proof idea:** If adding an edge,  $e$ , would create a cycle, then we maintain relation  $p^*$  – the path relation without the edge completing the cycle – as well as  $E^*$ ,  $E$  and  $D$ .

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Using Thm. 2, the above methodology has been extended to cyclic deterministic graphs.

- ▶ Itzhaky, Banerjee, Immerman, Nanevski, Sagiv, “Effectively-Propositional Reasoning About Reachability in Linked Data Structures” CAV 2013.
- ▶ Itzhaky, Banerjee, Immerman, Lahav, Nanevski, Sagiv, “Modular Reasoning about Heap Paths via Effectively Propositional Formulas”, POPL 2014

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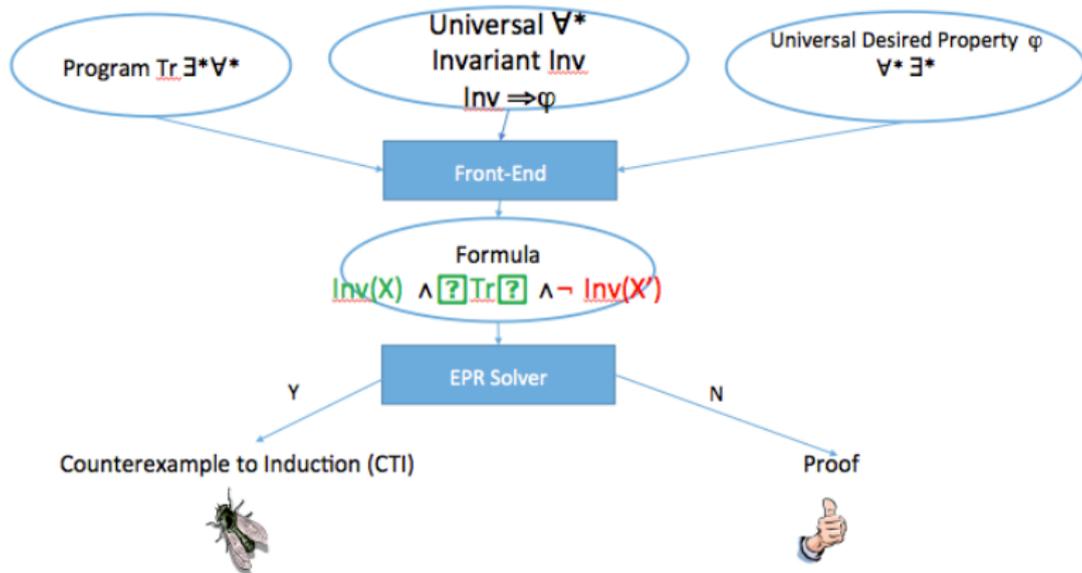
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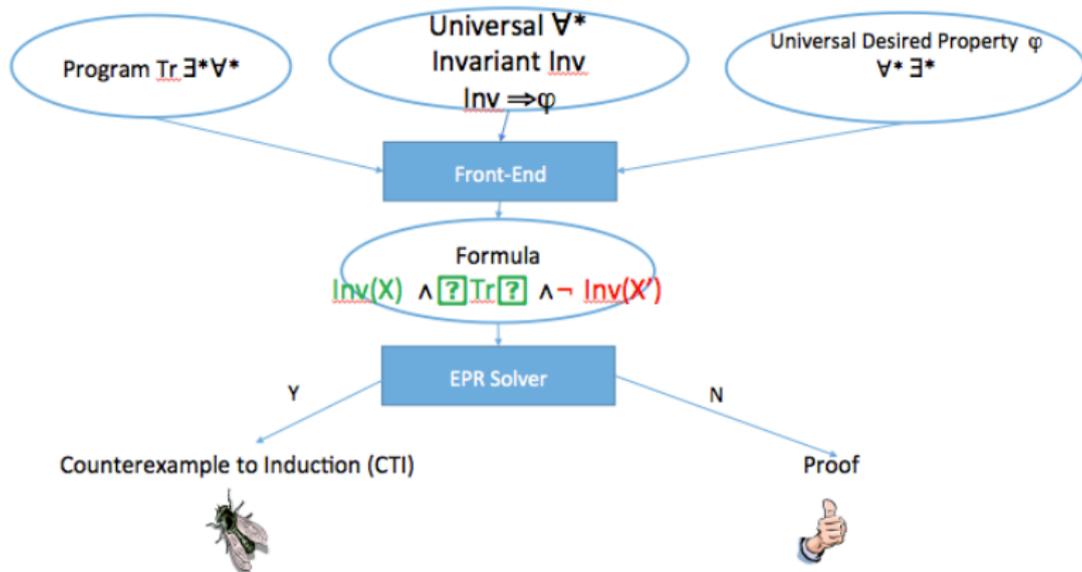
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- ▶ Karbyshev, Bjorner, Itzhaky, Rinetzky, Shoham, “Property-Directed Inference of Universal Invariants or Proving Their Absence” [CAV15].

# Deductive verification by reductions to EPR

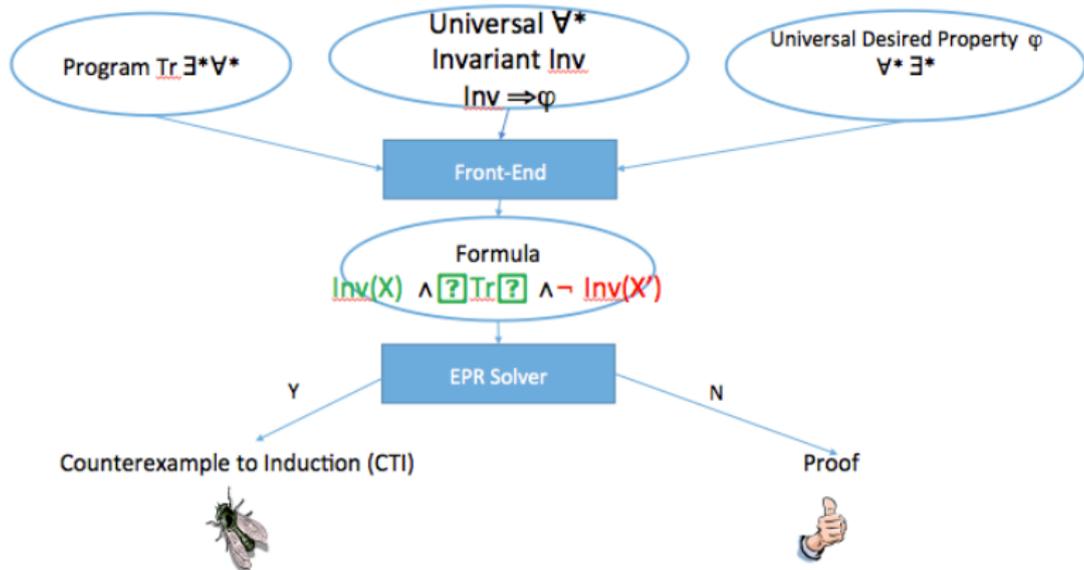


## Deductive verification by reductions to EPR



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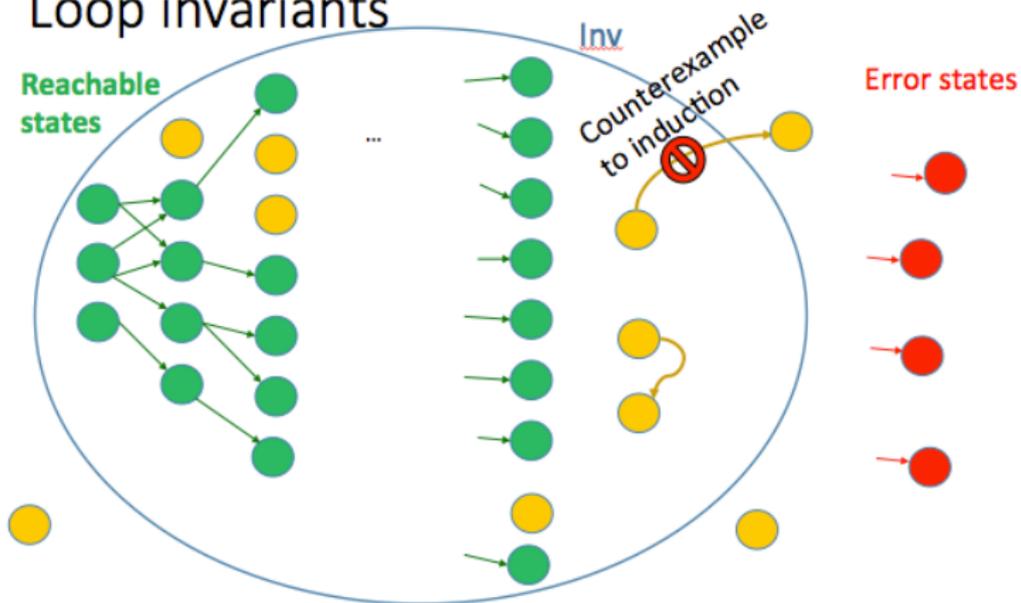
## Deductive verification by reductions to EPR



- ▶ When does this work?
- ▶ When doesn't this work?

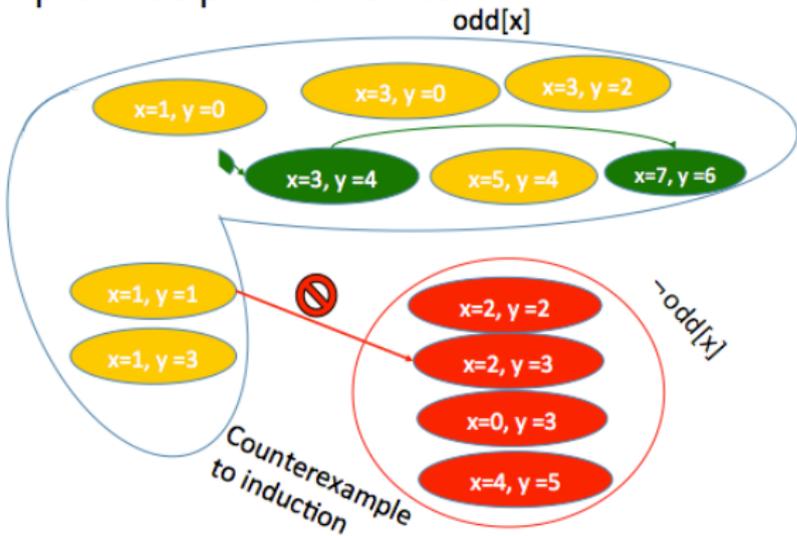
- $\text{Init} \rightarrow \text{Inv}$ ;  $\text{Inv} \wedge \text{Tr} \rightarrow \text{Inv}'$ ;  $\text{Inv} \rightarrow \text{Safe}$

## Loop invariants



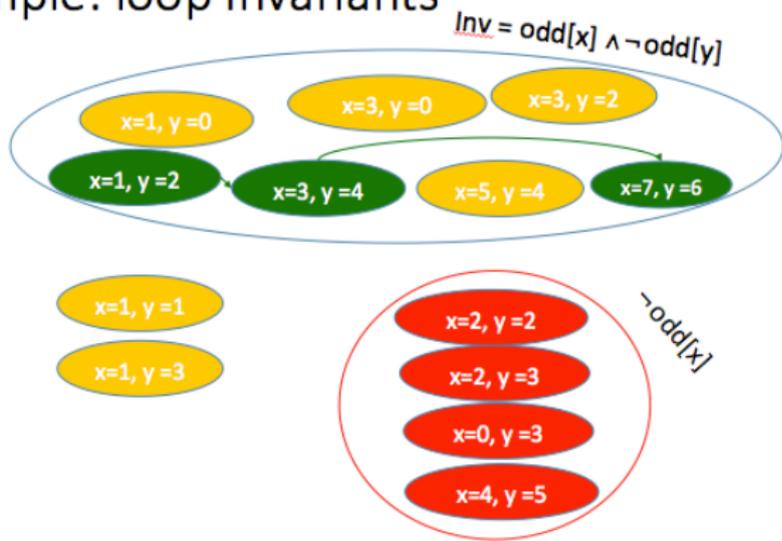
## Simple Example: loop Invariants

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1: x := 1;
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- ▶ Can Understand Decidability of Checking FO Inductive Invariants, via bounded depth of nesting of functions in  $\bar{t}$  needed for unsatisfiability.

# Thank You!

Anindya Banerjee, Bill Hesse,  
Yotam Feldman, Shachar Itzhaky,  
Aleksandr Karbyshev, Ori Lahav,  
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